## Notice of References Cited Application/Control No. 10/748,241 Examiner Stanetta D. Isaac Applicant(s)/Patent Under Reexamination JEONG HO PARK Page 1 of 1

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name ·	Classification
	Α	US-6,171,916	01-2001	Sugawara et al.	438/303
	В	US-5,780,340	07-1998	Gardner et al.	438/259
	O	US-6,040,212	03-2000	Kim, Hyun-Chul	438/242
	D	US-5,429,970	07-1995	Hong, Gary	438/259
	E	US-5,998,288	12-1999	Gardner et al.	438/589
	F	US-			-
	G	US-			
	Н	US-			
	1	US-			
	J	US-			
	к	US-			
	L	US-			
		US-			

## FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					_
	0					
	Р					_
	Q					
	R					
	s					
	Т					

## **NON-PATENT DOCUMENTS**

	_	TOTAL ATENT DOCUMENTO
*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf and Richard N. Tauber, Silicon Processing For The VLSI Era, 1986, Lattice Press, Vol. I, pgs. 551-555
	V	
	w	
	<u>k</u>	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.